

Start End Duration Topic

Monday, the 5th of October			
Generalities on converter control and initiation to RT-LAB			
13:00	14:00		Welcome and registration
14:00	14:30	00:30	Presentation of the participants and the staff and General introduction to the workshop
14:30	16:00	01:30	DC/DC converter model and generalities on control tuning
16:00	16:15	00:15	Break
16:15	18:00	01:45	Initiation to RT-LAB - Simple simulation
	Total	04:00	

Tuesday, the 6th of Octobre 2026, Morning			
Lecture on RT and A simple case study: a DC/DC converter			
08:15	09:45	01:30	Initiation to RT-LAB - IOs Management - Converter simulation
09:45	10:30	00:45	performance
10:30	10:45	00:15	Break
10:45	11:30	00:45	performance
11:30	12:00	00:30	Introduction to the micro-controller Texas Instrument and rapid prototyping with Matlab 1/2
	Total	03:45	

Lunch

Tuesday, the 6th of Octobre 2026, Afternoon			
A simple case study: a DC/DC converter HiL			
13:30	16:00	02:30	Introduction to the micro-controller Texas Instrument and rapid prototyping with Matlab 2/2
16:00	16:15	00:15	Break
16:15	18:00	01:45	DC/DC converter control in HiL
	Total	04:30	

Wednesday, the 7th of Octobre 2026, Morning			
Test on the Experimental Board from DC/DC control to Grid-forming control			
08:30	09:30	01:00	Experimental board presentation and control in DC/DC mode 1/2
09:30	10:00	00:30	Break
10:00	11:00	01:00	Experimental board presentation and control in DC/DC mode 2/2
11:00	12:15	01:15	Grid forming control (theoretical part)
	Total	03:45	

Lunch

Wednesday, the 7th of Octobre 2026, Afternoon			
Grid-Forming Control - HiL and full implementation on the experimental board			
13:30	15:00	01:30	Grid forming control (simulation)

15:00	16:00	01:00	AC/DC converter Full Real-Time simulation - Dynamic performance
16:00	16:30	00:30	Break
16:30	18:00	01:30	HiL test of Grid-Forming controlled VSC 1/2
	Total	04:30	

Diner

Thursday, the 8th of Octobre 2026, Morning			
AC/DC control on the experimental board and lab-tour			
08:15	09:00	00:45	HiL test of Grid-Forming controlled VSC 2/2
09:00	10:15	01:15	Full implementation on the experimental board
10:15	10:30	00:15	Break
10:30	11:30	01:00	Full implementation on the experimental board
11:30	12:30	01:00	Lab Tour
	Total	04:15	