

Monday, the 15th of September 2025			
Grid-Forming Control - Generalities			
13:00	14:00		Welcome and registration
14:00	14:15	00:15	Presentation of the participants and the staff
14:15	14:30	00:15	General introduction to the workshop
14:30	15:30	01:00	DC/DC converter model and generalities on control tuning
15:30	16:45	01:15	Grid forming control (theoretical part)
16:45	17:00	00:15	Break
17:00	18:00	01:00	Grid forming control (simulation)
	Total	04:00	

Tuesday, the 16th of September 2025, Morning			
Initiation to RT-LAB			
08:30	10:00	01:30	Initiation to RT-LAB - Simple simulation
10:00	10:30	00:30	Break
10:30	12:00	01:30	Initiation to RT-LAB - IOs Management - Converter simulation
	Total	03:30	

Lunch

Tuesday, the 16th of September 2025, Afternoon			
A simple case study: a DC/DC converter			
13:30	14:45	01:15	DC/DC converter Full Real-Time simulation - Dynamic performance
14:45	16:15	01:30	Introduction to the micro-controller Texas Instrument and rapid prototyping with Matlab 1/2
16:15	16:45	00:30	Break
16:45	18:15	01:30	Introduction to the micro-controller Texas Instrument and rapid prototyping with Matlab 2/2
	Total	04:45	

Wednesday, the 17th of September 2025, Morning			
Test on the Experimental Board from DC/DC control to Grid-forming control			
08:30	10:30	02:00	DC/DC converter control in HiL
10:30	11:00	00:30	Break
11:00	12:00	01:00	Experimental board presentation and control in DC/DC mode
	Total	03:30	

Lunch

Wednesday, the 17th of September 2025, Afternoon			
Grid-Forming Control - HiL and full implementation on the experimental board			
13:30	14:30	01:00	AC/DC converter Full Real-Time simulation - Dynamic performance
14:30	16:00	01:30	Introduction on starting process (pre-charging, synchronization,...), basic protection rules
16:00	16:30	00:30	Break
16:30	17:30	01:00	HiL test of Grid-Forming controlled VSC 1/2
	Total	04:00	

Diner

Thursday, the 18th of September 2025, Morning			
AC/DC control on the experimental board and lab-tour			
08:30	09:15	00:45	HiL test of Grid-Forming controlled VSC 2/2
09:15	10:15	01:00	Full implementation on the experimental board
10:15	10:30	00:15	Break
10:30	11:30	01:00	Full implementation on the experimental board
11:30	12:30	01:00	Lab Tour
	Total	04:00	